

REMARKS

The present filing is responsive to the Office Action.

Summary of the Response

No claims have been amended. Claims 1-12 remain pending in this application. Reexamination and reconsideration of the present application as amended are respectfully requested.

Claim Rejections Under 35 USC 103

Claims 1-4 and 10-12 are rejected under 35 U.S.C 103(a) as being unpatentable over Ochi et al. (US 2003/0107537) in view of Nakamura (US 6,867,757; "Nakamura I"). Claims 5-9 are rejected under 35 U.S.C 103(a) as being unpatentable over Ochi and Nakamura (US 6,867,757) in further view of Nakamura (US 2003/0043132; "Nakamura II"). These rejections are respectfully traversed.

On the outset, Applicant notes that given the traversal of Ochi below, Applicant has not yet considered the possibility of swearing behind Ochi, but reserves the right to do so if it becomes necessary.

In the office action, the Examiner acknowledged that Ochi does not teach the display pixels of each colour in a row are associated with a respective and separate power line (26', 26", 26""), and wherein the power supply to each power line is individually switchable (40, 45, 48) so as to control the duty cycle of the associated display pixels. The Examiner turned to Nakamura I

for the missing teaching, referring to Fig. 1 therein. Applicant respectfully disagrees. Nakamura I does not make up for the deficiencies of Ochi.

Nakamura I does not disclose controlling the duty cycle of the associated display pixels by individually switching the power supply to each power line for the serially connected drive transistor and display element of the display pixels of a particular colour. Referring to Nakamura I at column 3, lines 38-55, and Fig. 1, it discloses a driving circuit that individually adjusts duty cycle or outputs from the PWMs 105, 106 and 107 to control the LED driving transistors 108, 109 and 110 respectively to change average currents flowing through the different color LEDs 101, 102 and 103. The power supply lines to the LEDs 101, 102 and 103 in series with the transistors 108, 109 and 110 are not individually switched. In other words, the duty cycles of the PWMs are individually controlled, which outputs control signal controlling the driving of the transistors for the LEDs. A VDD is commonly connected to the LEDs, which VDD is not switched for each individual LED. It is the driving transistors 108, 109 and 110 that change the average current to the LEDs.

In contradistinction, the recited invention individually switches the power supply to each power line for pixels (each pixel comprising serially connected driving transistor and display element) of the same color in a row. There is no indication anywhere in Nakamura I that the power supply to each power line is individually switchable for pixels of the same color in a row. Nakamura I therefore does not teach the recited individually switchable power lines.

Accordingly, the combination of Ochi and Nakamura, even if somehow feasible and reasonable, would not obtain the claimed invention. Further, there is no reason to make the combination in the first place. Ochi specifically does not require individually switching power supply lines to the LEDs, or PWM control of its driving transistors. Absent any teaching,

suggestion, motivation or apparent reason to modify Ochi with individually switchable power lines to achieve expected or predictable results, it would not have been obvious to one skill in the art to refer to Nakamura I to do so, even assuming Nakamura I actually teaches the modification suggested only by the Examiner.

Independent claim 1 and all claims dependent therefrom are therefore not rendered obvious by Ochi and Nakamura I. Nakamura II does not make up for the deficiencies of Ochi and Nakamura I.

CONCLUSION

In view of all the foregoing, Applicant submits that the claims pending in this application are patentable over the references of record and are in condition for allowance. Such action at an early date is earnestly solicited. **The Examiner is invited to call the undersigned representative to discuss any outstanding issues that may not have been adequately addressed in this response.**

The Assistant Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this transmittal and associated documents, or to credit any overpayment to **Deposit Account No. 501288** referencing the attorney docket number of this application.

Respectfully submitted,



Wen Liu

Registration No. 32,822

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LIU & LIU
444 S. Flower Street; Suite 1750
Los Angeles, California 90071
Telephone: (213) 830-5743
Facsimile: (213) 830-5741
Email: wliu@liulaw.com